

Web [Images](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more](#) ▾

[Sign in](#)

Google

I/O completion port alert interrupt awake threads   [Advanced Search](#)  
[Preferences](#)

Web

Results 1 - 10 of about 1,130 for **I/O completion port alert interrupt awake threads**. (0.24 seconds)

Did you mean: [I/O completion port alert interrupt awake threads](#)

### Walk-the-Plank Bugs

Each time the system timer **interrupts**, the **interrupt** handler walks through the queue ...

Triggering a driver to unload while in an **I/O completion** routine. ...

[www.wd-3.com/archive/WalkPlank.htm](http://www.wd-3.com/archive/WalkPlank.htm) - 29k - [Cached](#) - [Similar pages](#)

### (WO/2006/083836) TRANSMIT COMPLETION EVENT BATCHING

If the NICs receive **completion** event for the packet from network **port** 2 were to ..... in conjunction with the issuance of an **interrupt/wake-up** event, ...

[www.wipo.int/pctdb/en/wo.jsp?IA=WO2006083836&DISPLAY=DESC](http://www.wipo.int/pctdb/en/wo.jsp?IA=WO2006083836&DISPLAY=DESC) - 141k -

[Cached](#) - [Similar pages](#)

### Implementing the New Win32 Driver Model for Windows 98 and Windows ...

Suppose your microwave oven uses an **interrupt** and a set of **I/O** ports. Your StartDevice routine would need to determine the **port** addresses for later use and ...

[www.microsoft.com/msj/1297/win32driver2.aspx](http://www.microsoft.com/msj/1297/win32driver2.aspx) - 73k - [Cached](#) - [Similar pages](#)

### [PDF] Intel 82801AA (ICH) & Intel 82801AB (ICH0) I/O Controller Hub AC '97

File Format: PDF/Adobe Acrobat - [View as HTML](#)

**Alert** on LAN is a result of the Intel-IBM Advanced Manageability Alliance and is a trademark of ..... maximum response latency (**interrupt**, **thread**, etc. ...

[www.intel.com/design/chipsets/manuals/29802801.pdf](http://www.intel.com/design/chipsets/manuals/29802801.pdf) - [Similar pages](#)

### SysOpt.com - Award Bios Triton Manual

The following is a list of IRQ's, **Interrupt** ReQuests, which can be exempted much as COM ports and LPT ports above can. When an **I/O** device wants to gain the ...

[www.sysopt.com/tutorials/article.php/3552541](http://www.sysopt.com/tutorials/article.php/3552541) - 119k - [Cached](#) - [Similar pages](#)

### Software controlled content addressable memory in a general ...

Input/output **completion port** queue data structures and methods for using same ..... can be provided to **alert** the **thread** that the data is available or has ...

[www.patentstorm.us/patents/6868476-description.html](http://www.patentstorm.us/patents/6868476-description.html) - 98k - [Cached](#) - [Similar pages](#)

### Sram controller for parallel processor architecture including a ...

Input/output **completion port** queue data structures and methods for using same ..... will then **alert** the **thread** that the data is available or has been sent. ...

[www.freepatentsonline.com/7305500.html](http://www.freepatentsonline.com/7305500.html) - 175k - [Cached](#) - [Similar pages](#)

### [PDF] -----Offload Sockets Framework and Sockets Direct Protocol High ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

OSF enables applications to use these same standard sockets and File **I/O** ..... in a high-priority **thread** context such as **interrupt** level. ...

[infiniband.sourceforge.net/archive/OSF\\_SDP\\_HLD.pdf](http://infiniband.sourceforge.net/archive/OSF_SDP_HLD.pdf) - [Similar pages](#)

### TaoSecurity: 01/01/2004 - 02/01/2004

Thanks to this **thread** I learned how to install a single **port** that doesn't ..... for the poor performance figures is something called **interrupt** livelock. ...

[taosecurity.blogspot.com/2004\\_01\\_01\\_archive.html](http://taosecurity.blogspot.com/2004_01_01_archive.html) - 211k - [Cached](#) - [Similar pages](#)

### [PDF] I/O Kit Device Driver Design Guidelines

File Format: PDF/Adobe Acrobat

PCI device or any driver that interacts directly with an **interrupt** controller ..... kernel

<http://www.google.com/search?sourceid=navclient&ie=UTF-8&rls=GGLD,GGLD:2004-30,GGLD:en&q=...> 12/27/07

controls the **thread**. **I/O completion** routines execute outside the ...  
developer.apple.com/documentation/DeviceDrivers/  
Conceptual/WritingDeviceDriver/WritingDeviceDriver.pdf - [Similar pages](#)

Did you mean to search for: [I/o completion port alert interrupt awake threads](#)

1 2 3 4 5 6 7 8 9 10 **Next**

Try [Google Desktop](#): search your computer as easily as you search the web.

---

I/o completion port alert interrupt aw:  Search

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#) | [Try Google Experimental](#)

---

©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)